X-exam in Mechatronics 2003-05-30

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a)

What is FEM?

Grades: 0-9p: 1, 10-19p: 2, 20-29: 3, 30-39: 4, 40-50: 5. 1 a) What is the difference between directives, standards and harmonized standards?(1p) Identify three ways to minimize ground impedance in circuits. b) (2p) In what end of a shielded cable is it recommendable to ground the shield, if only the c) equipment in one end is grounded. (2p)2 a) In what order does the following items appear in a project: A: Initiation, B: Implementation and Construction, C: Comissioning and handover, D: Design and development. (2p) What is a "baseline" in a project? b) (2p) Explain the entities Earned value, Budget at completition, Slippage and Overrun. (2p) c) 3 In what order does the followin items appear in a Development process: A: Detailed a) Design, B: Production ramp-up; C: Concept development, D: System level design, E: Planning, F: Testing and refinement. (2p) Name five (5) steps in a concept generation procedure. b) (2p) 4 What is the difference between a crystalline and amorf structure of a material? (2p) a) Define the entities "Modulus of elasticity", "Yield strength" and "Tensile strength" b) by drawing a stress-strain-curve. (2p) Rank the following materials in terms of "Youngs modulus"; Elastomeres, Nylons, c) Glass, Steel, Ceramics. (2p)Name at least 5 different methods for shaping an object. d) (2p) 5 Propose one method to minimize inductances in capacitive circuits. a) (2p) b) What is a servo motor? (2p) c) Name two position sensor types. (2p) d) Name two current sensor types/principles. (2p) 6 How many layers constitutes the OSI-model for communication? a) (2p) What is the difference between a LAN and a Fieldbus? (2p) b) Name the most important features of Terfenol-D. c) (2p) What is a FPGA? Describe with a few words. d) (1p)What is a PLC? Describe with a few words. e) (1p) What is a DSP? Describe with a few words. f) (1p)Name at least three possible reasons for delays and jitter in real time control g) systems. (2p) What is Java? h) (2p)

(2p)

- Define the words *Preprocessor*, *Solver* and *Postprocessor*. What does "integrated analysis" mean in a design process? (2p) (2p) b) c)